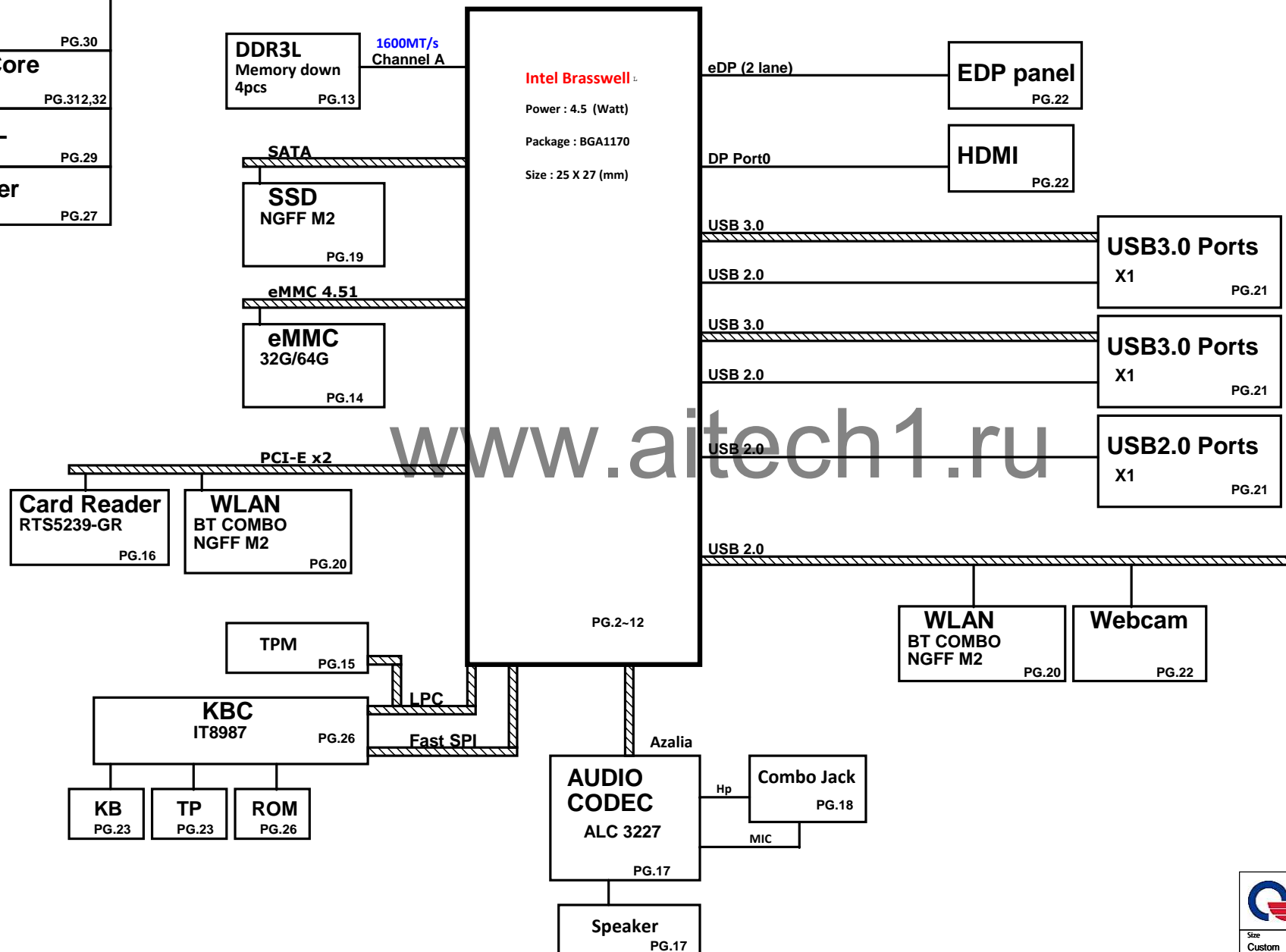
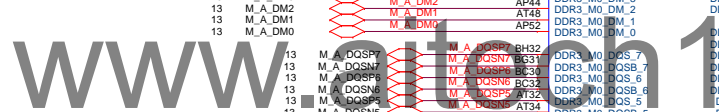


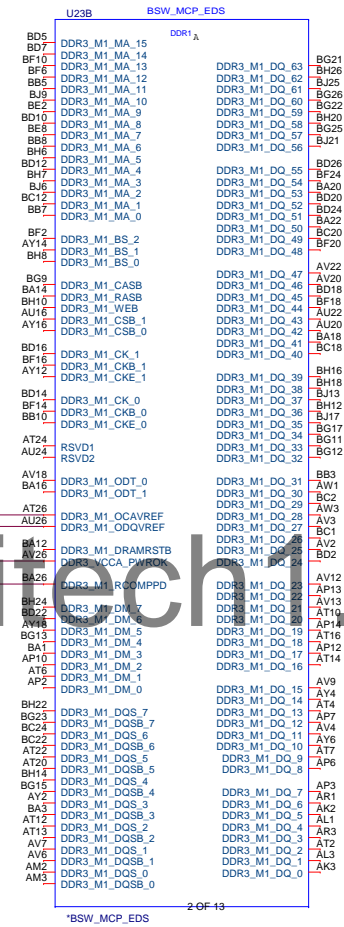
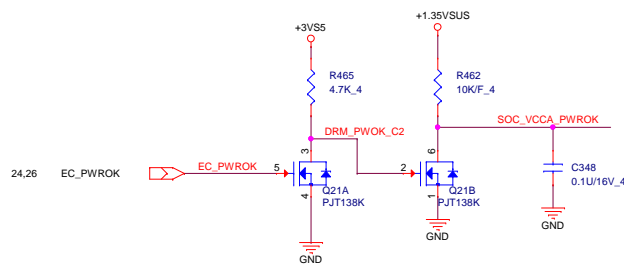
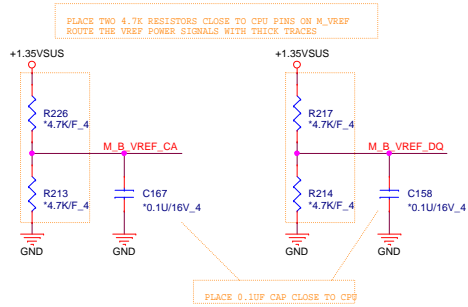
OP9 (14")

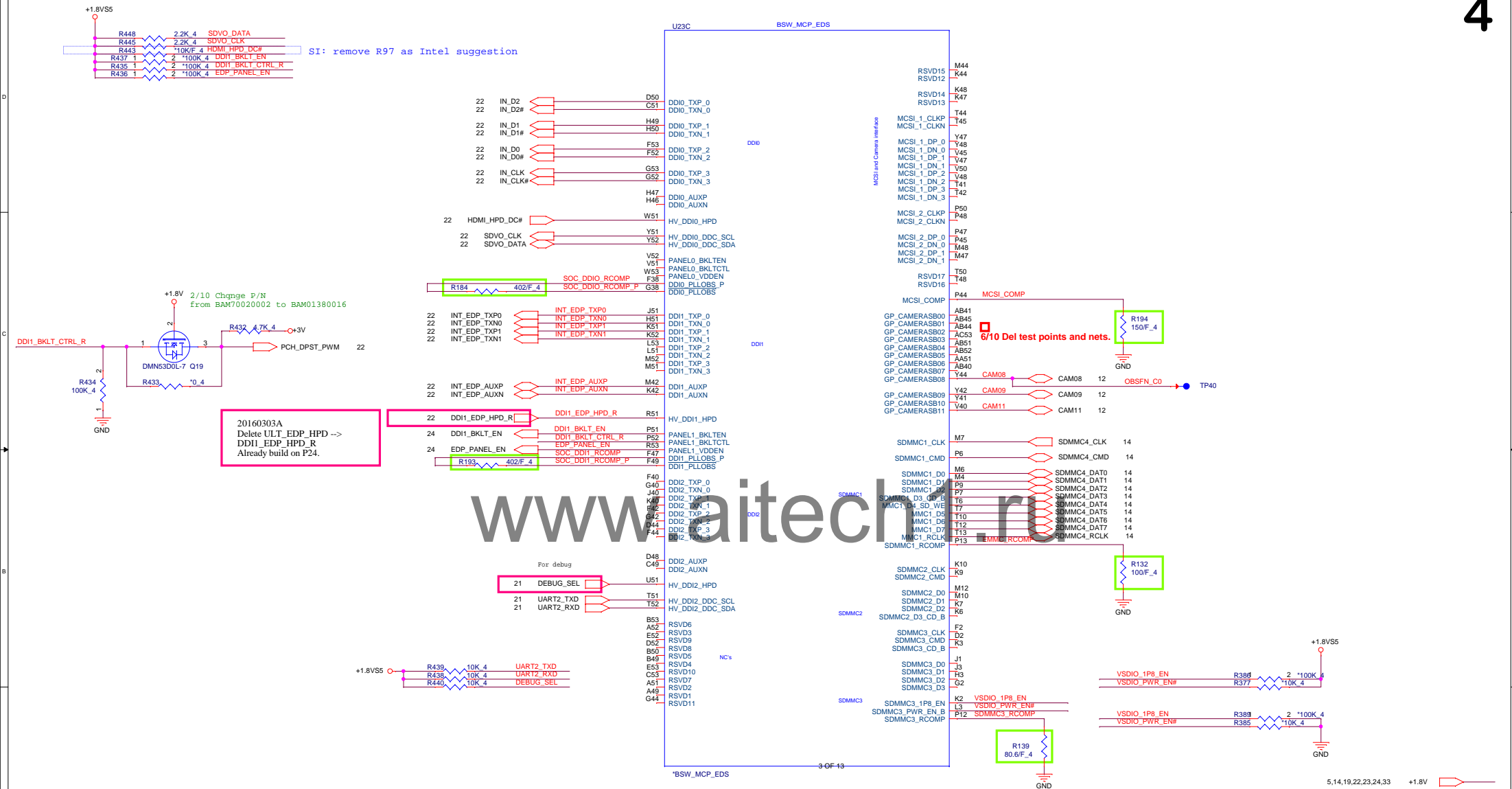
Intel Brasswell-M Platform Block Diagram

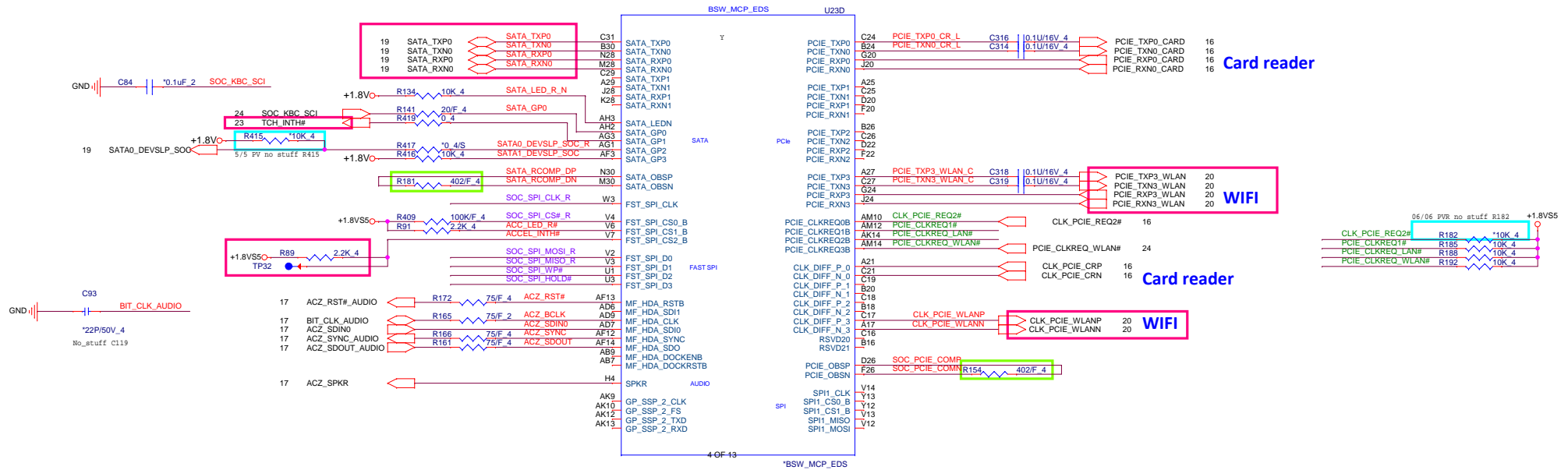
+3VS5/+5VS5	PG.28
MOIC	PG.30
CPU Core	PG.312,32
DDR3L	PG.29
Charger	PG.27



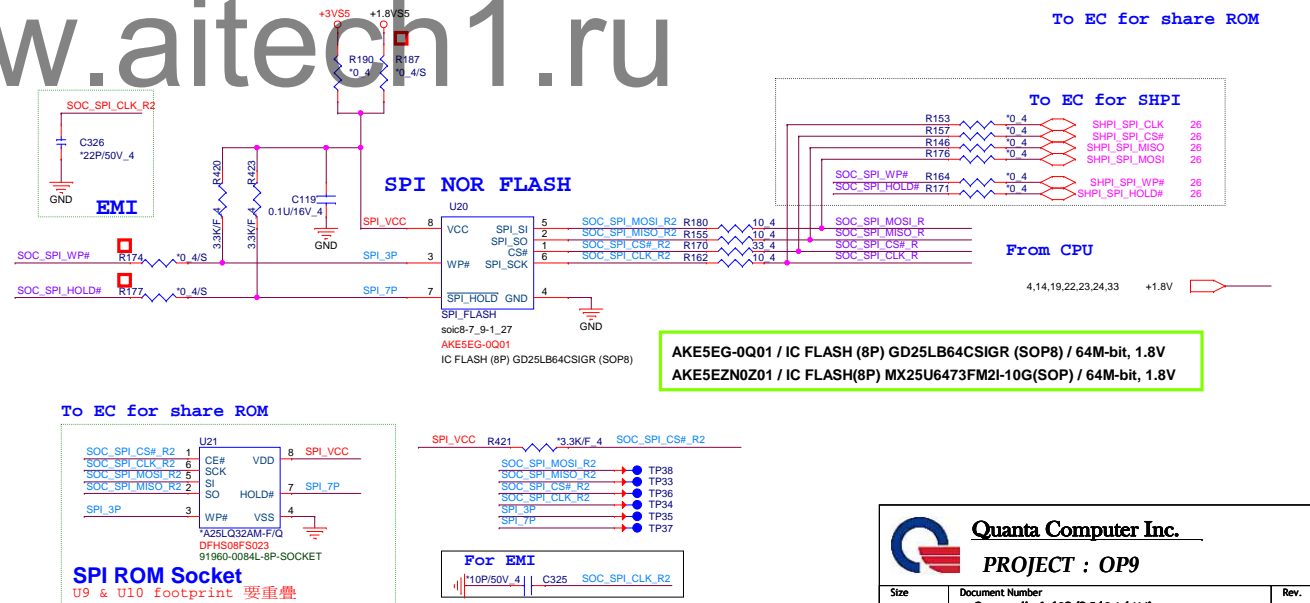
*BSW_MCP_EDS 1 OF 13







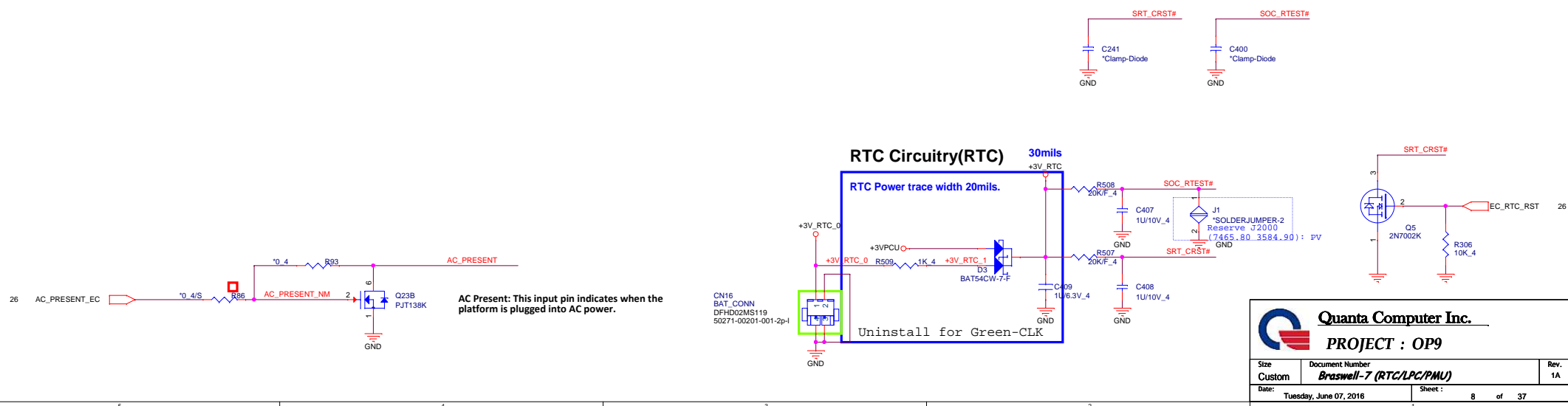
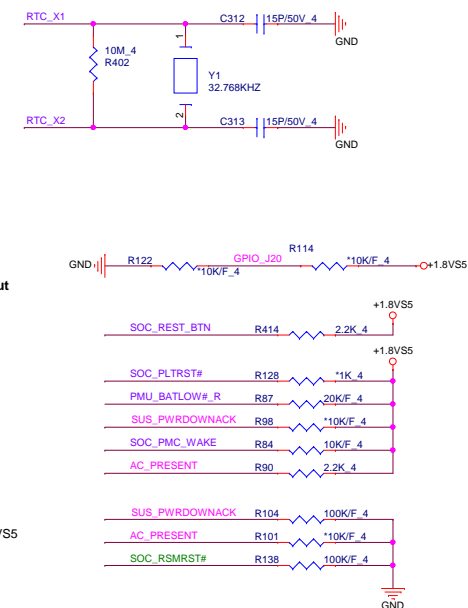
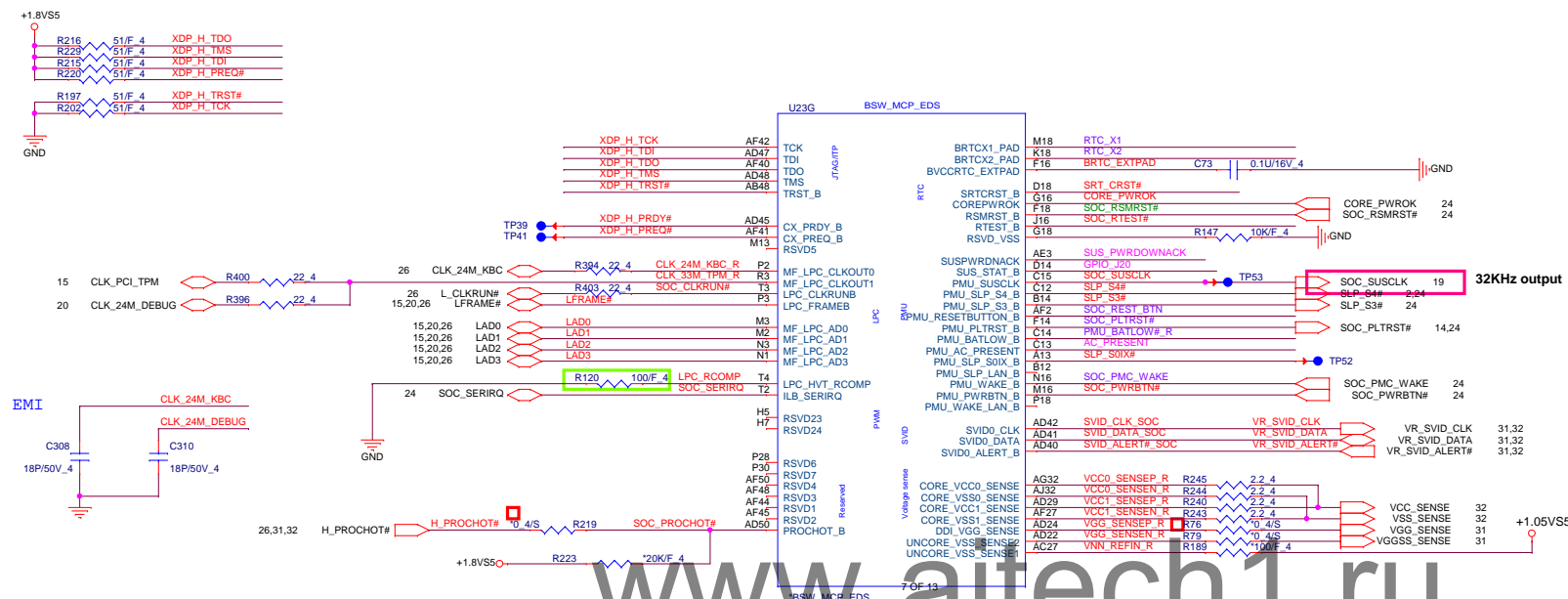
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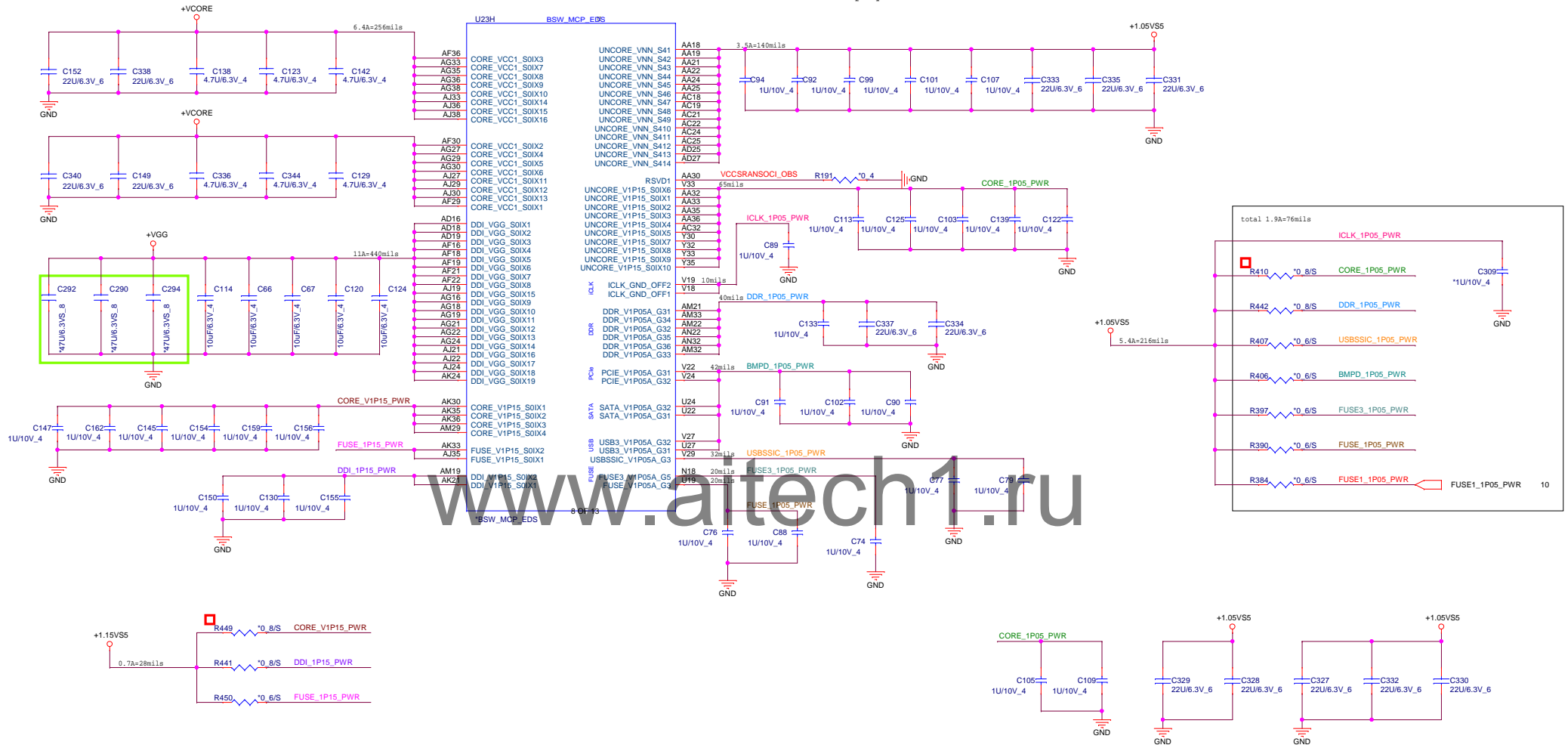


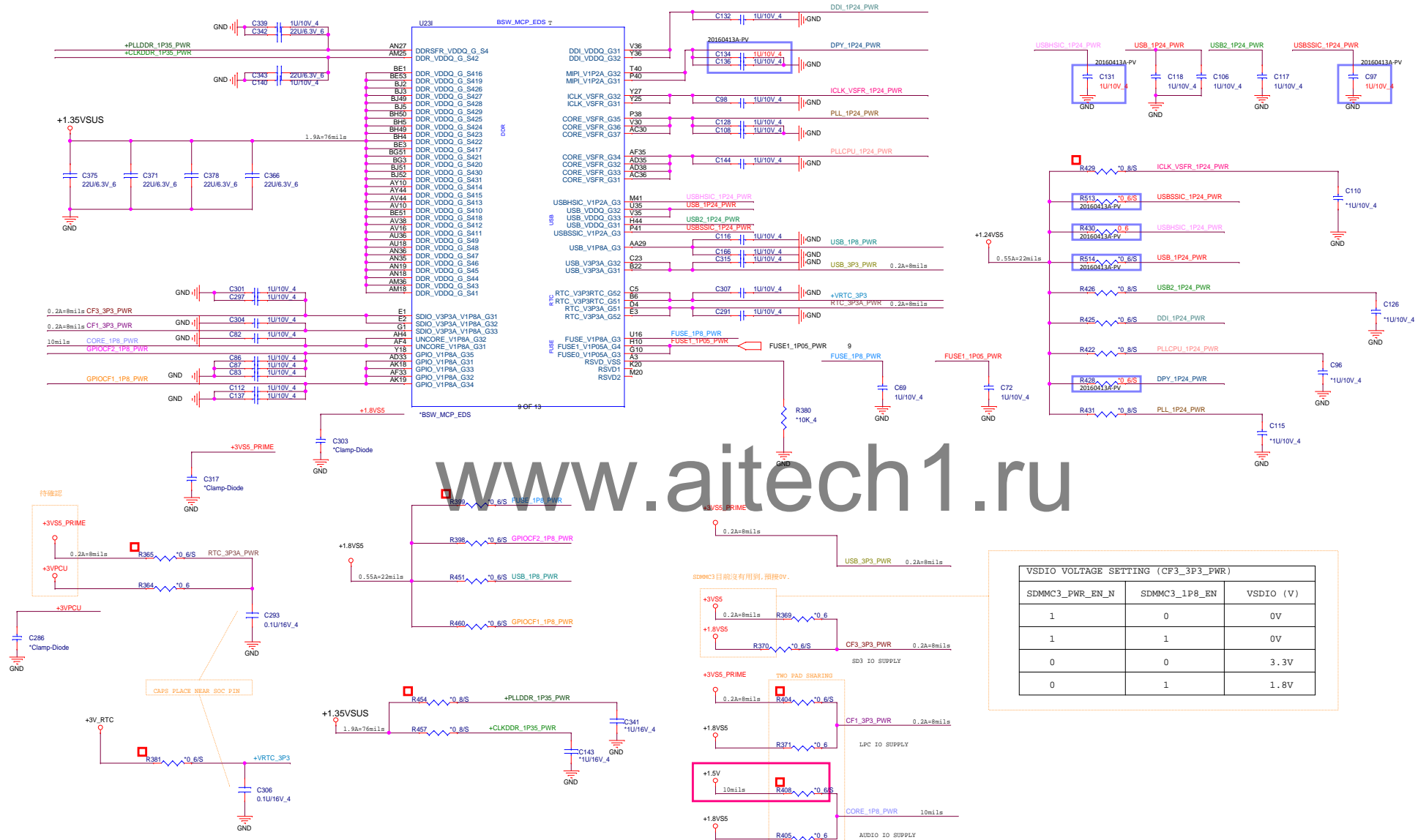
Size Custom	Document Number Braswell-5 (I2C/GPIO/CLK)	Rev. 1A
Date: Tuesday, June 07, 2016	Sheet : 6 of 37	

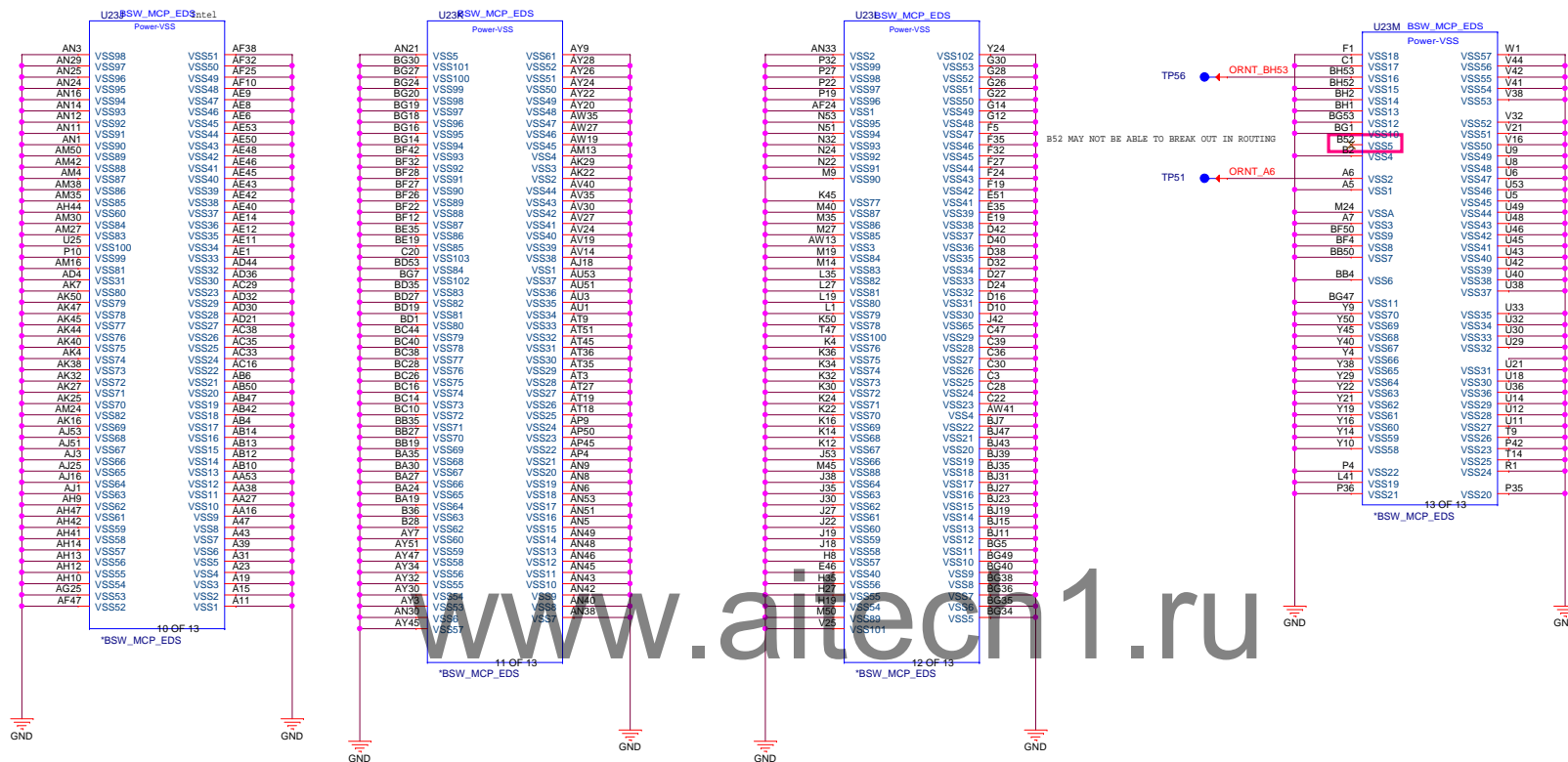
RTC Clock 32.768KHz

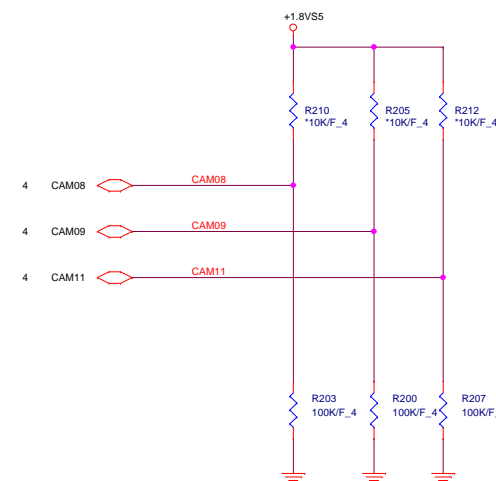


VNN can optionally be merged with V1P05A
if display resolution is 2560 x1600 @ 60Hz or lower.



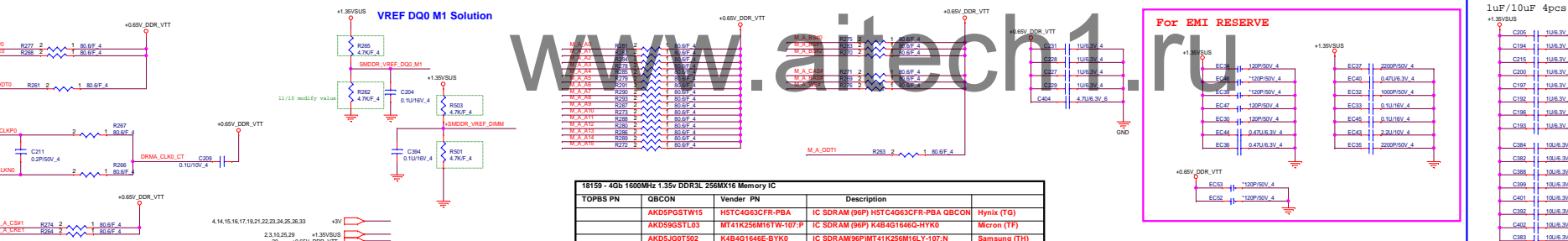
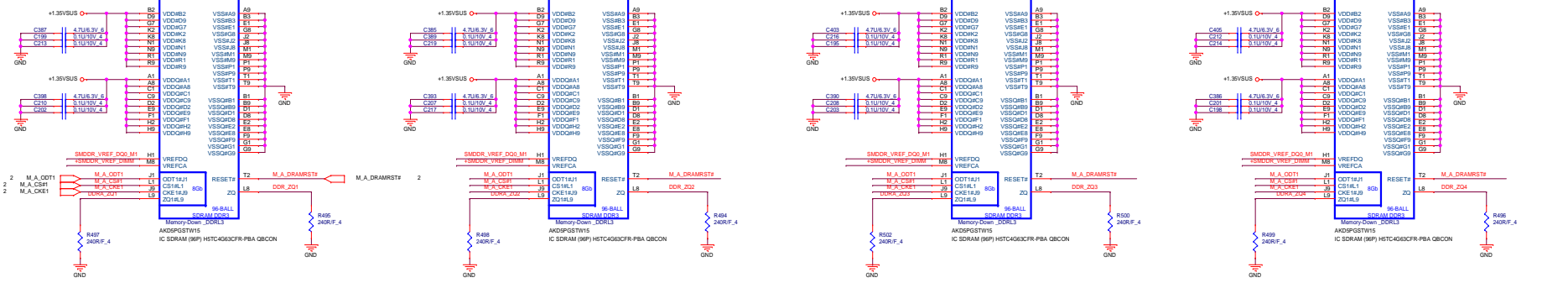






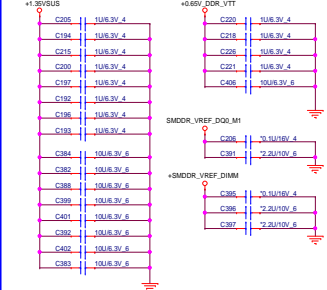
REQUIRED STRAPS

	GPIO_SUS0	GPIO_SUS1	TOP_SWAP	GPIO_SUS3	BIOS_STRAP	SOC_Override	GPIO_SUS6	SOC_KCB_SMI	GPIO_SUS8
PULL HIGH	DDI0 detected DEFAULT	DDI1 detected DEFAULT	Normal Operation DEFAULT	Reserve 10 KQ PU DEFAULT	SPI DEFAULT	Normal Operation	10 KQ PU to 1.8V DEFAULT	Reserve 10 KQ PU DEFAULT	Supply is 1.35V
PULL LOW	DDI0 not detected	DDI1 not detected	Change Boot Loader address		LPC	Override DEFAULT			Supply is 1.25V DEFAULT



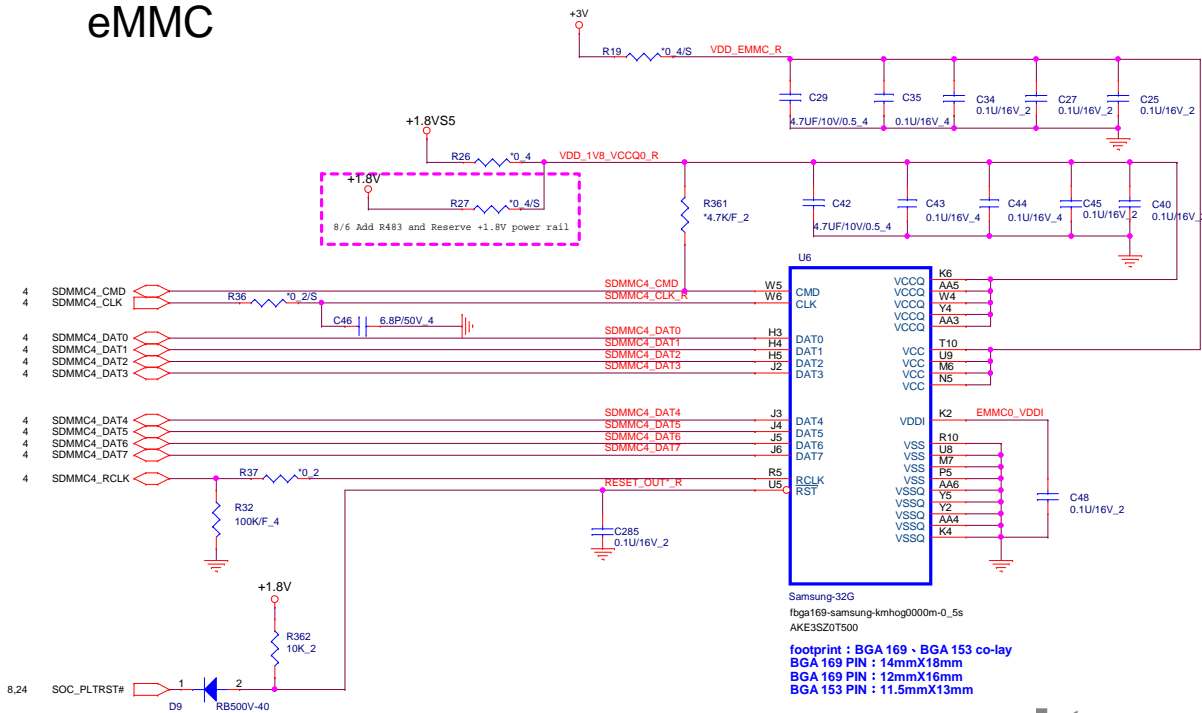
TOPBES PN	QBCON	Vendor PN	Description	
AKDPSGTW15	HSTC4G63CFR-PBA	IC SDRAM (6P) HSTC4G63CFR-PBA QBCON	Hynix (TG)	
AKDPSGTU03	MT41K1256MTW-107-P	IC SDRAM (6P) K4B4G1646G-HYK0	Micron (TF)	
AKDJSJGT502	K4B4G1646G-BYK0	IC SDRAM (6P) MT41K256M16LY-107-N	Samsung (TH)	

Place these Caps near So-Dimm0.
1uF/10uF 4pcs on each side of connector



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4,5,6,7,8,10,12,19,22,23,24,26,30,31
4,5,19,22,23,24,33
4,15,16,17,19,21,22,23,24,25,26,33

+1.8VSS
+1.8V
+3V


27741 - eMMC 32GB MO-276 MMCv5.0

TOPBSQ	QBICON	Description	SIZE	Vender
	AKE3S20T500	IC FLASH(153P) KLMBG2JENB-B041 QBICON	32G	Samsung
	AKE3SF-T106	IC FLASH(153P) SDINADF4-32G-H QBICON	32G	SanDisk

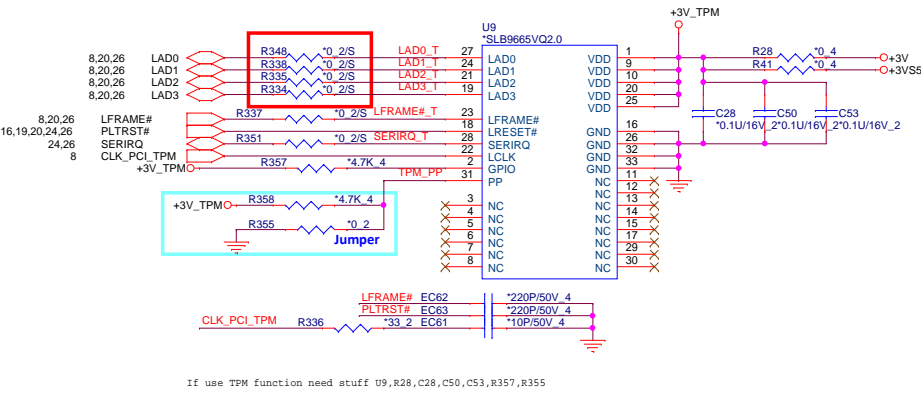
footprint : BGA 169 - BGA 153 co-lay
BGA 169 PIN : 14mmX18mm
BGA 169 PIN : 12mmX16mm
BGA 153 PIN : 11.5mmX13mm

eMMC setting		Location				
Vender	SIZE	R168 ~10K_4	R169 ~10K_4	R160 ~10K_4	R159 ~10K_4	R152 ~10K_4
Hynix	32G	1	0	0	0	0
samaung	32G	1	1	0	0	0
SanDisk	32G	1	1	1	0	0
Hynix	64G	1	1	1	1	0
samaung	64G	0	1	1	1	1
SanDisk	64G	0	0	1	1	1
Hynix (new)	32G	0	0	0	1	1
Hynix (new)	64G	0	0	0	0	1
		0	0	0	0	0
HDD		1	1	1	1	1

Memory setting		Location				
Vender	SIZE	R151 ~10K_4	R144 ~10K_4	R143 ~10K_4	R136 ~10K_4	R135 ~10K_4
Hynix	2G	1	0	0	0	0
samaung	2G	1	1	0	0	0
Micron	2G	1	1	1	0	0
Hynix	4G	1	1	1	1	0
samaung	4G	0	1	1	1	1
Micron	4G	0	0	1	1	1
samaung E	2G	0	0	0	1	1
Micron P	2G	0	0	0	0	1
		0	0	0	0	0
		1	1	1	1	1

			Quanta Computer Inc.	
			PROJECT : OP9	
Size Custom	Document Number Storage eMMC			Rev. 1A
Date: Tuesday, June 07, 2016		Sheet : 14 of 37		

TPM (2.0)



Accelerometer Sensor



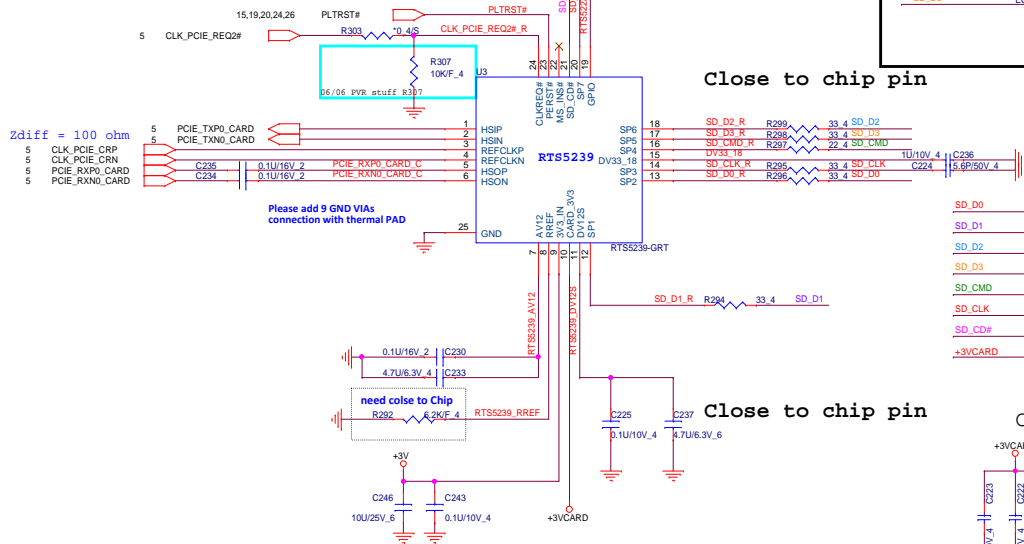
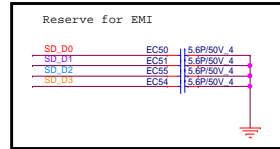
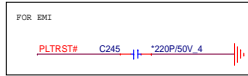
15

Touch screen

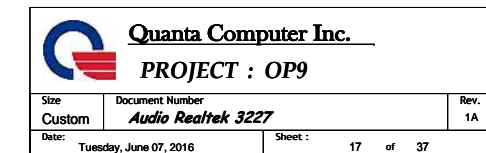
Green CLK Circuitry

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CardReader

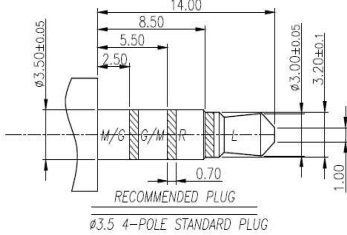
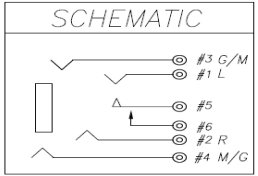
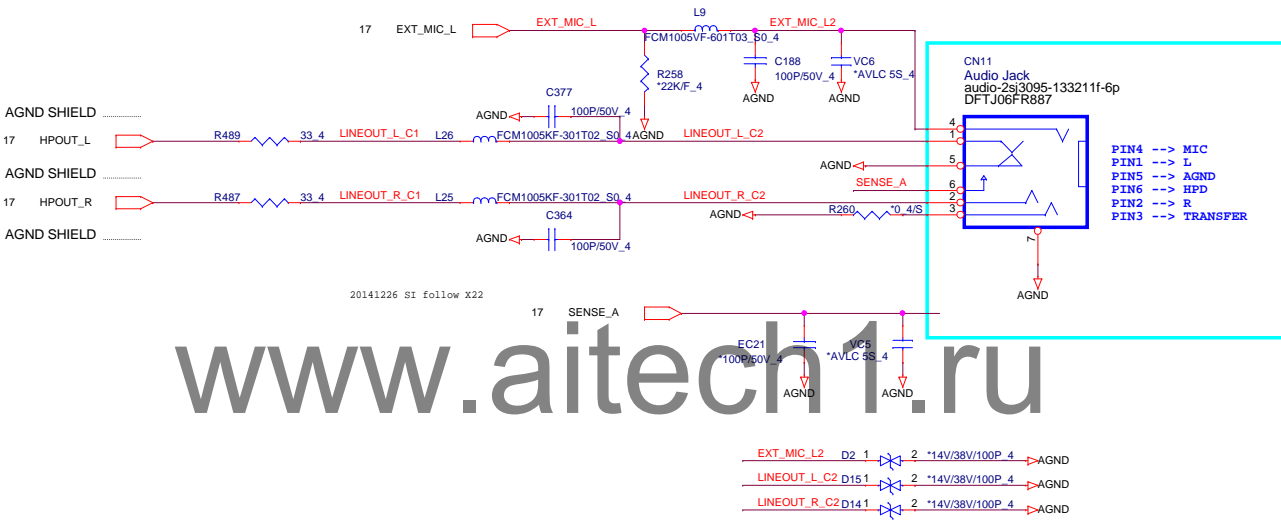


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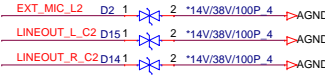


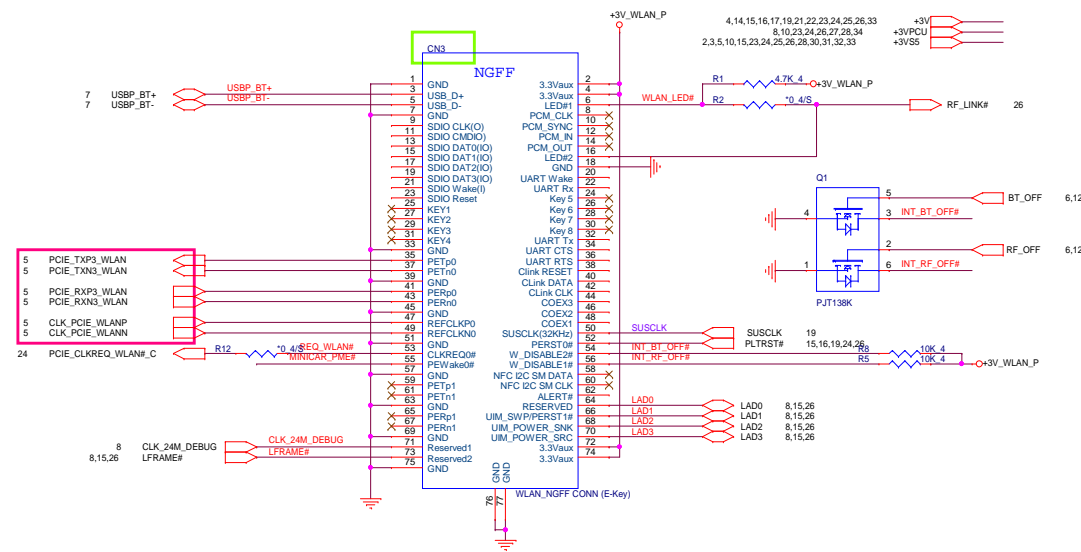
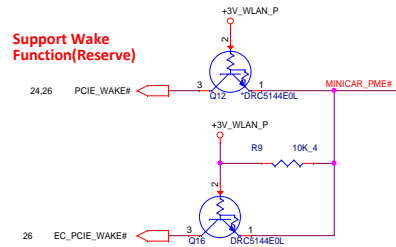
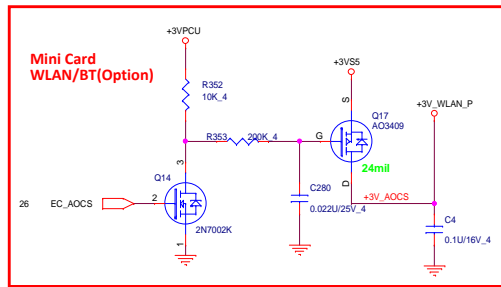
Head Phone out

17,22,33 +5V
4,14,15,16,17,19,21,22,23,24,25,26,33 +3V

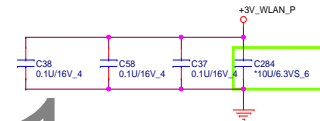
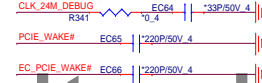


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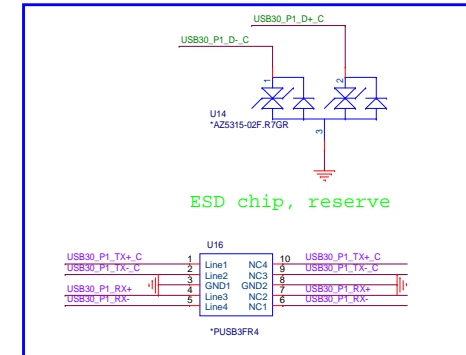
For EMI Suggestion



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UART for DEBUG

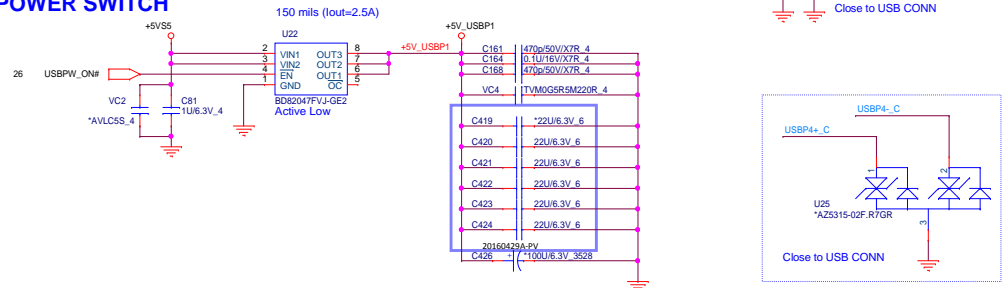
USB 3.0



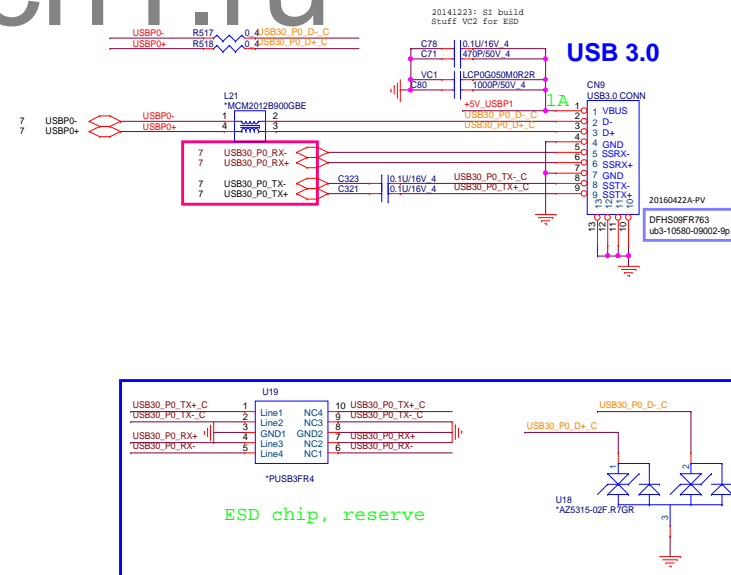
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USB 2.0 Only

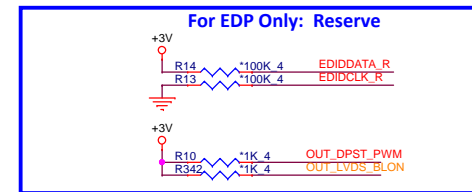
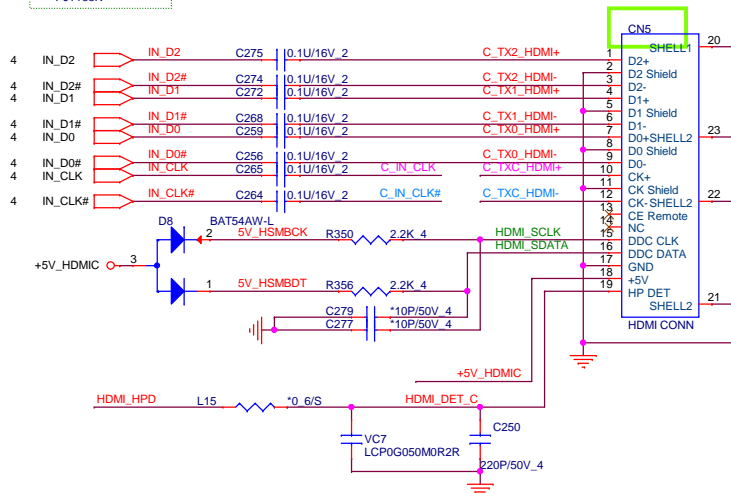
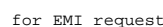
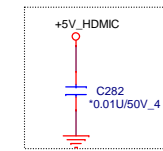
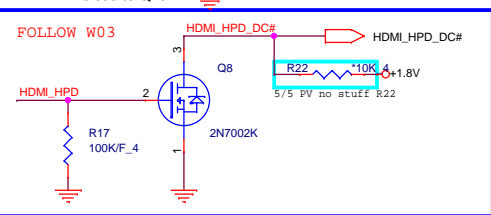
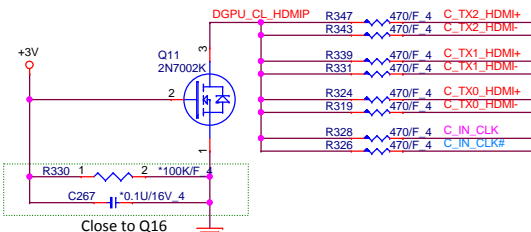
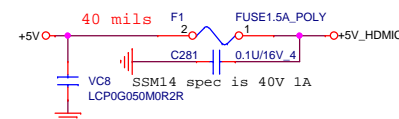
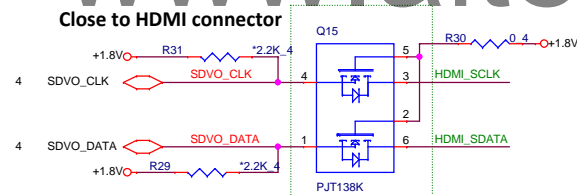
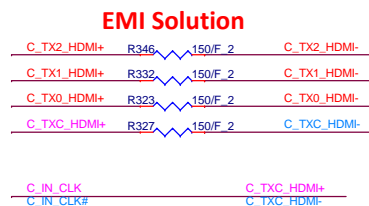
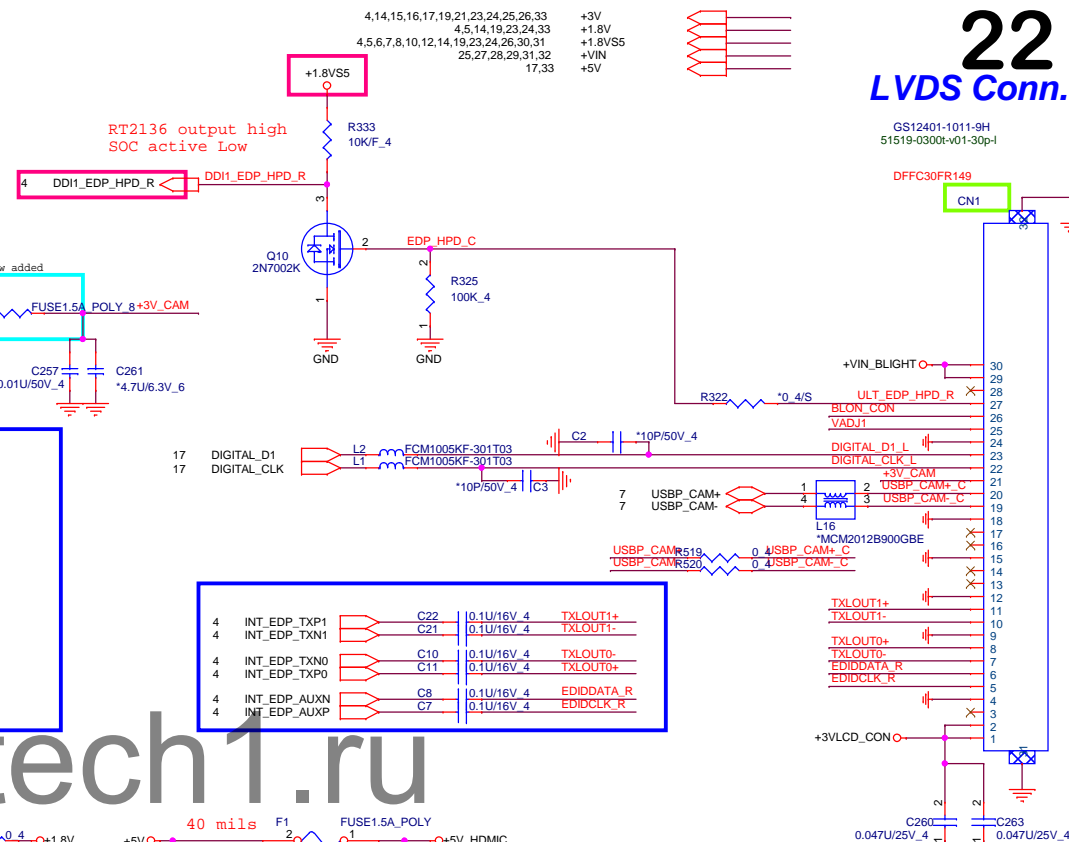
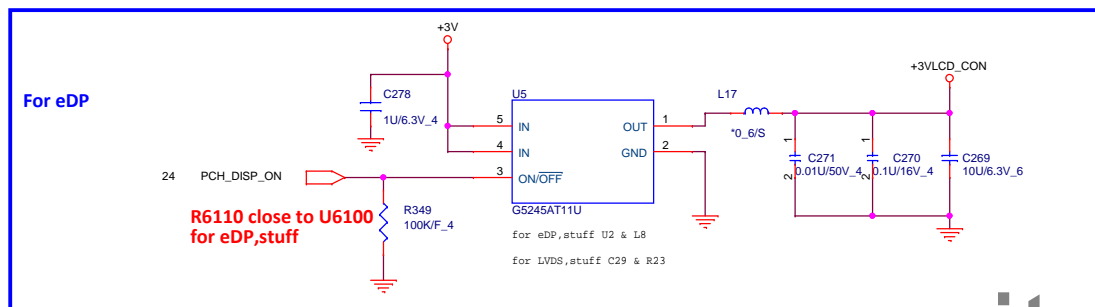
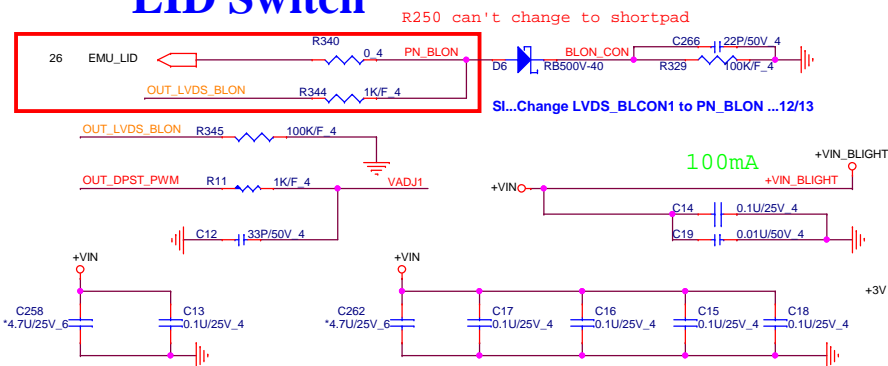
USB POWER SWITCH



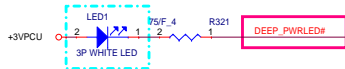
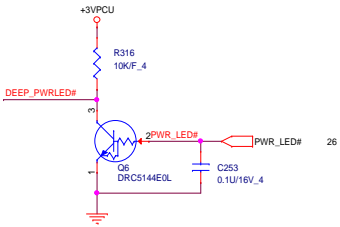
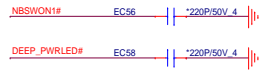
USB 2.0/3.0 Combo PORT0



LID Switch

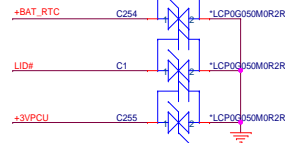
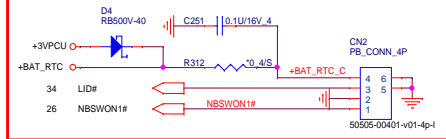


For EMI Suggestion

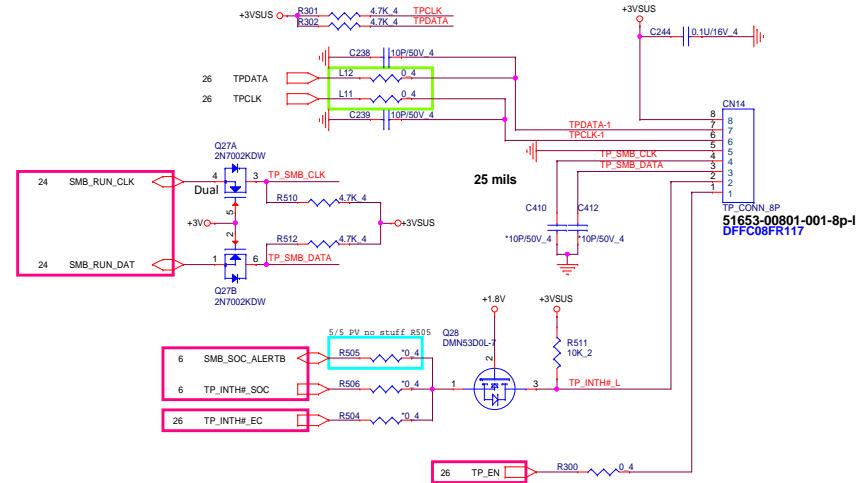


PN WAIT CONFIRM!

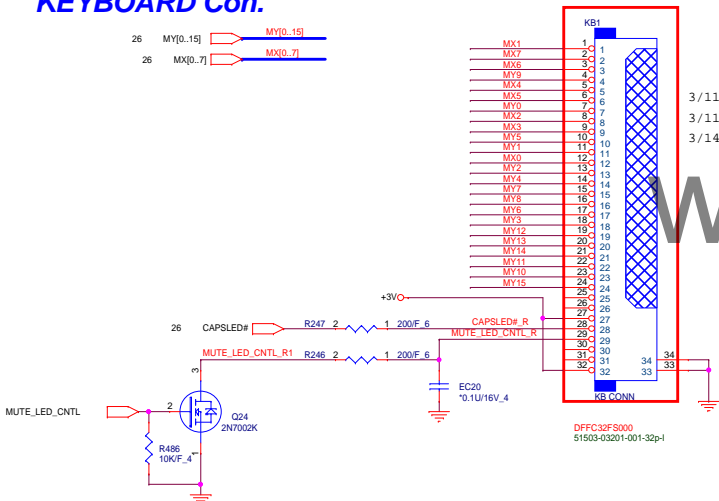
3/31 Update PN & FP (Change to 4p type)



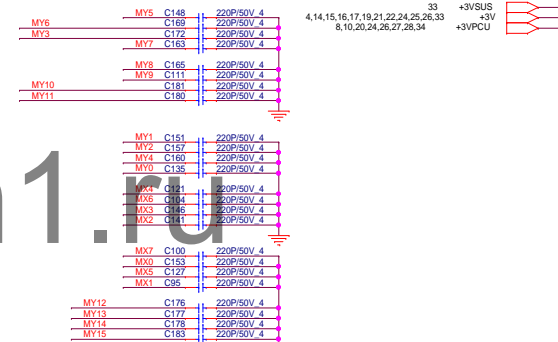
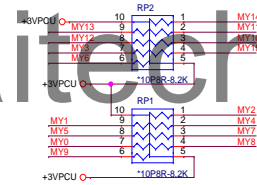
Touch Pad Connector



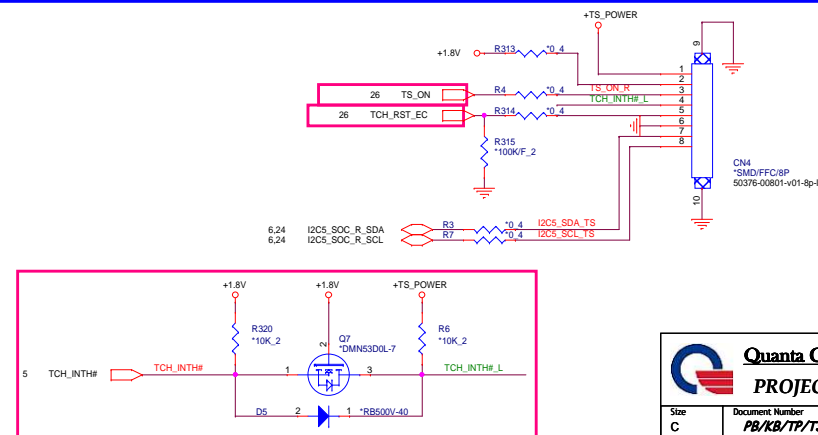
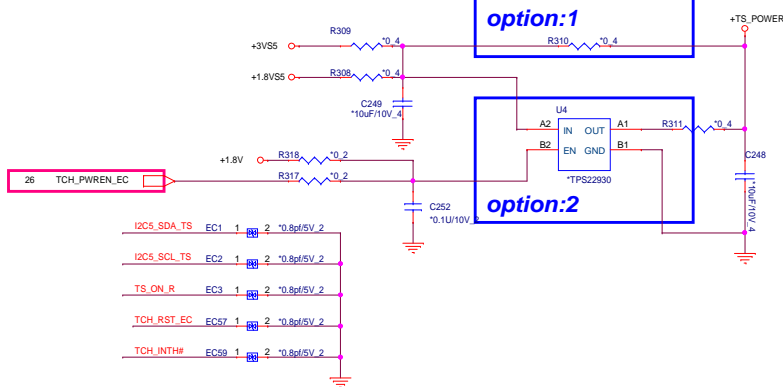
KEYBOARD Con.

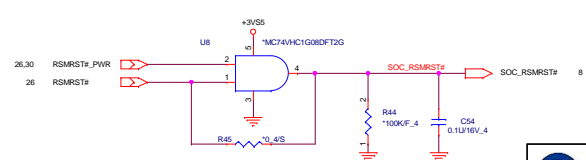
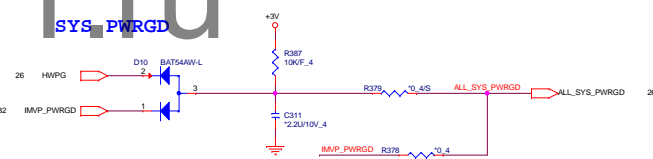
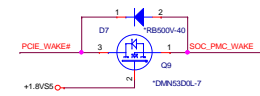
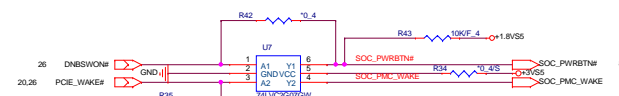
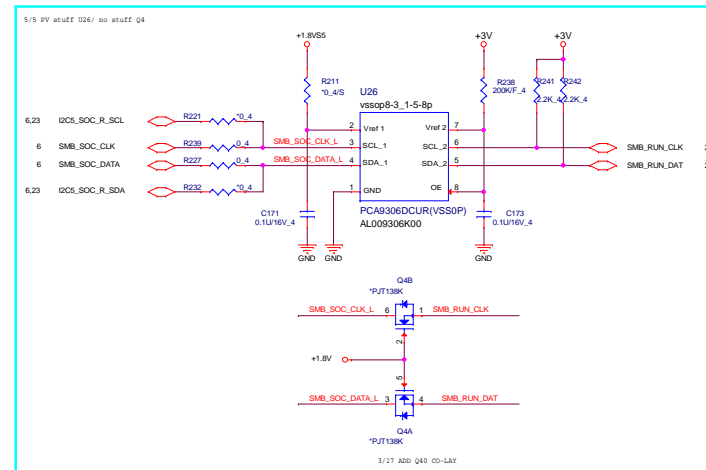
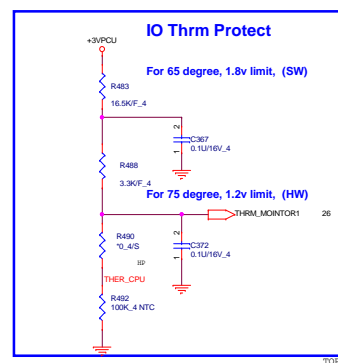
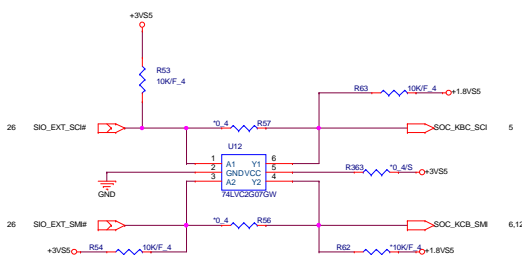
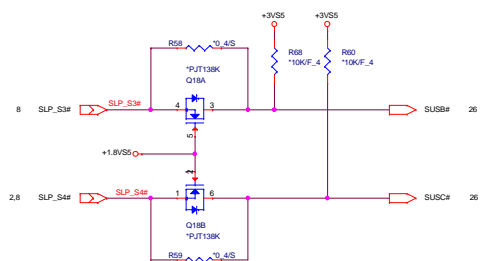
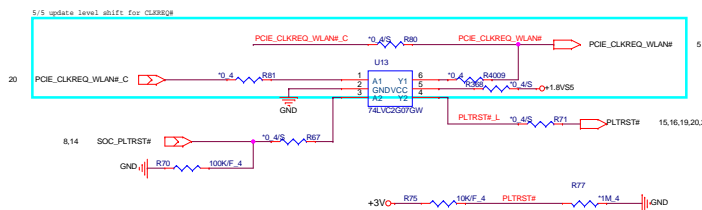
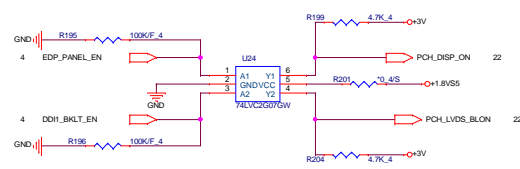
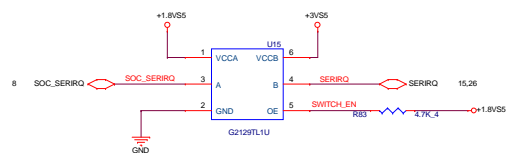


KEYBOARD PULL-UP

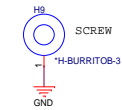
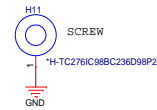
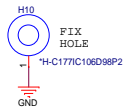
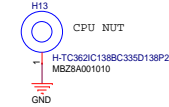
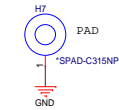
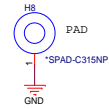
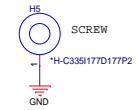
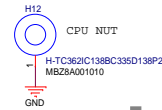
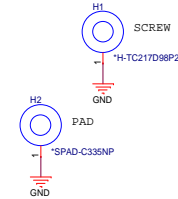
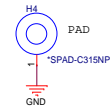
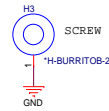
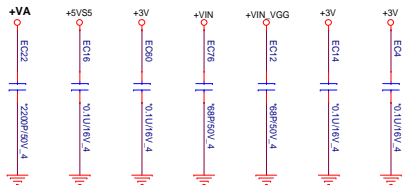
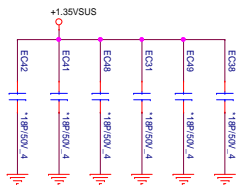
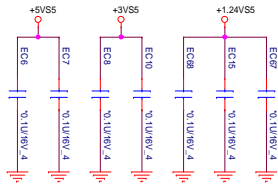
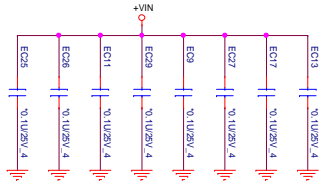


Touch Screen CN.



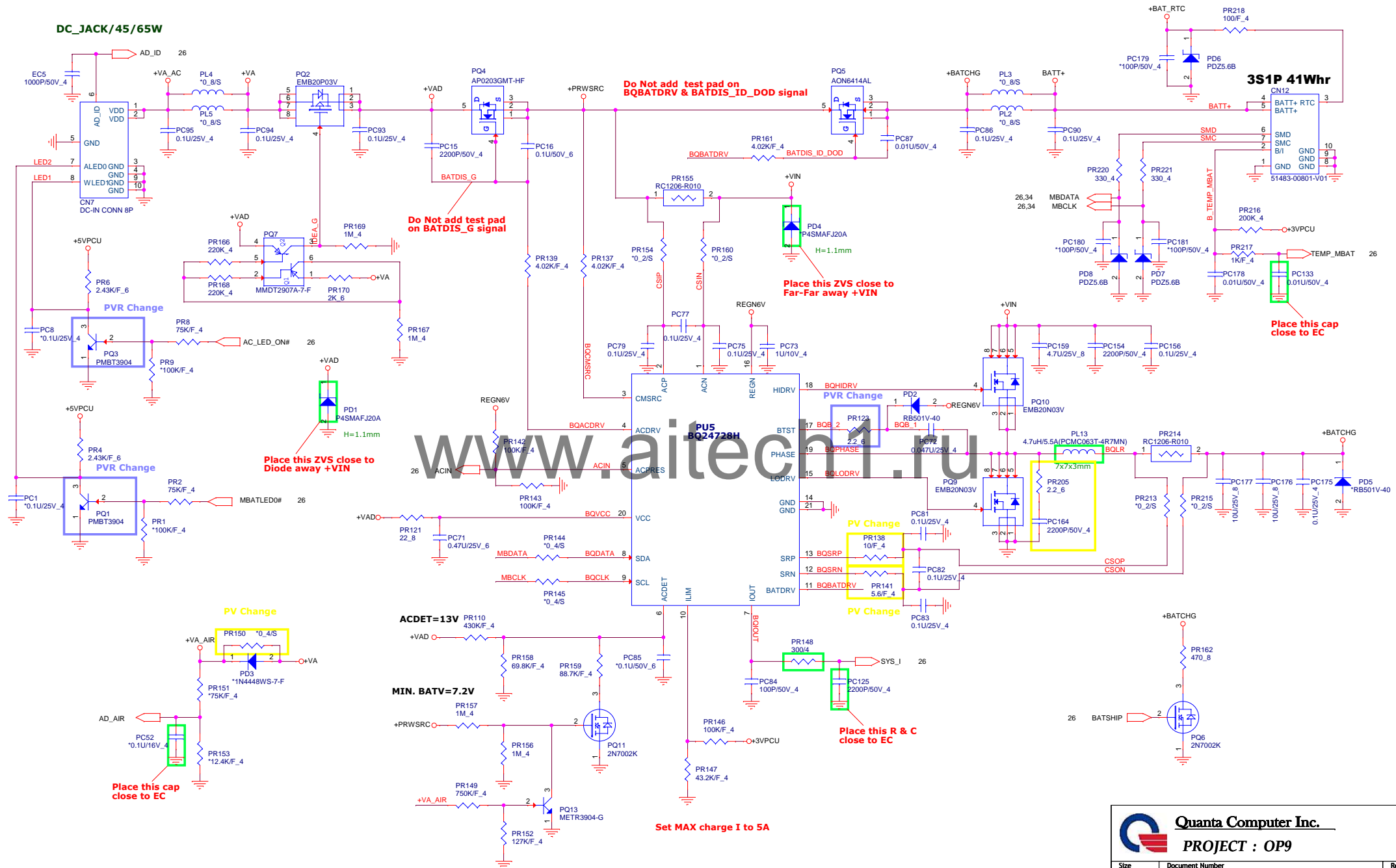


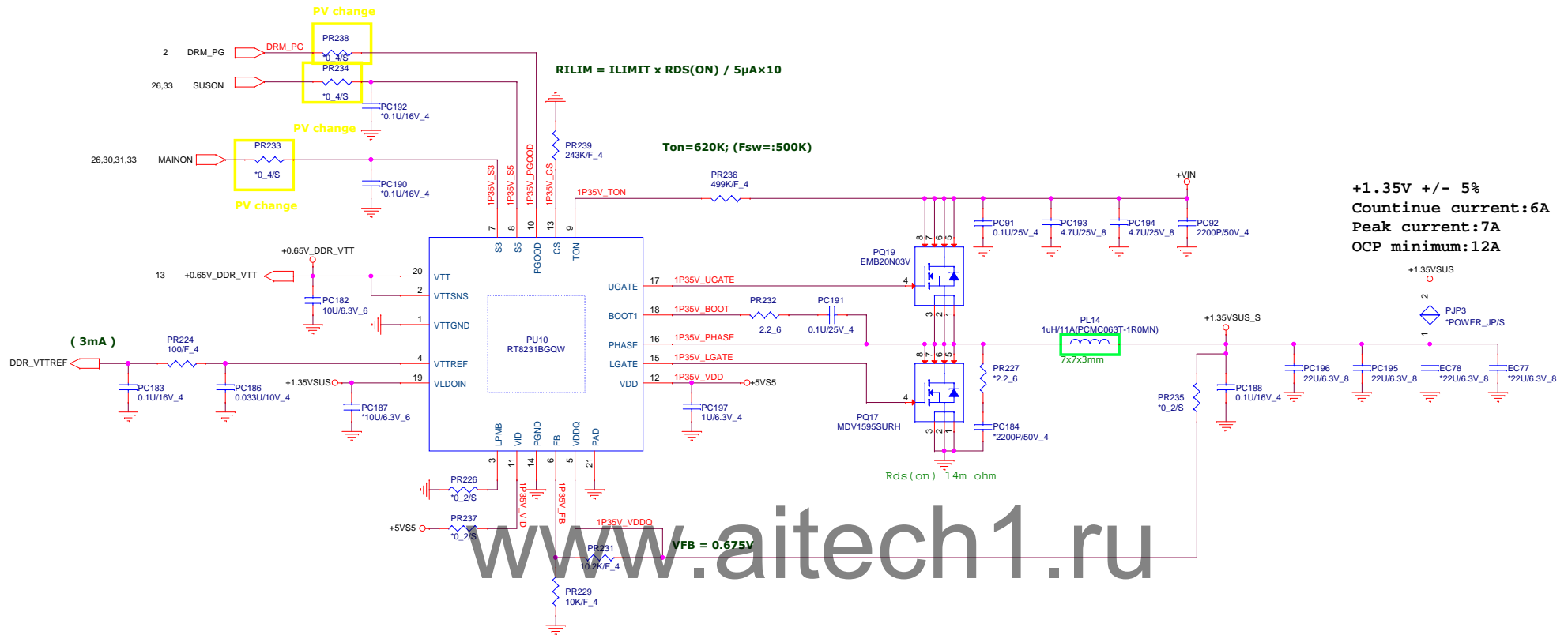
EMI Reserve

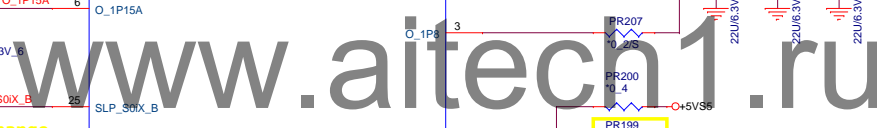


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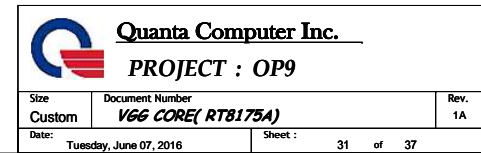
DC_JACK/45/65W

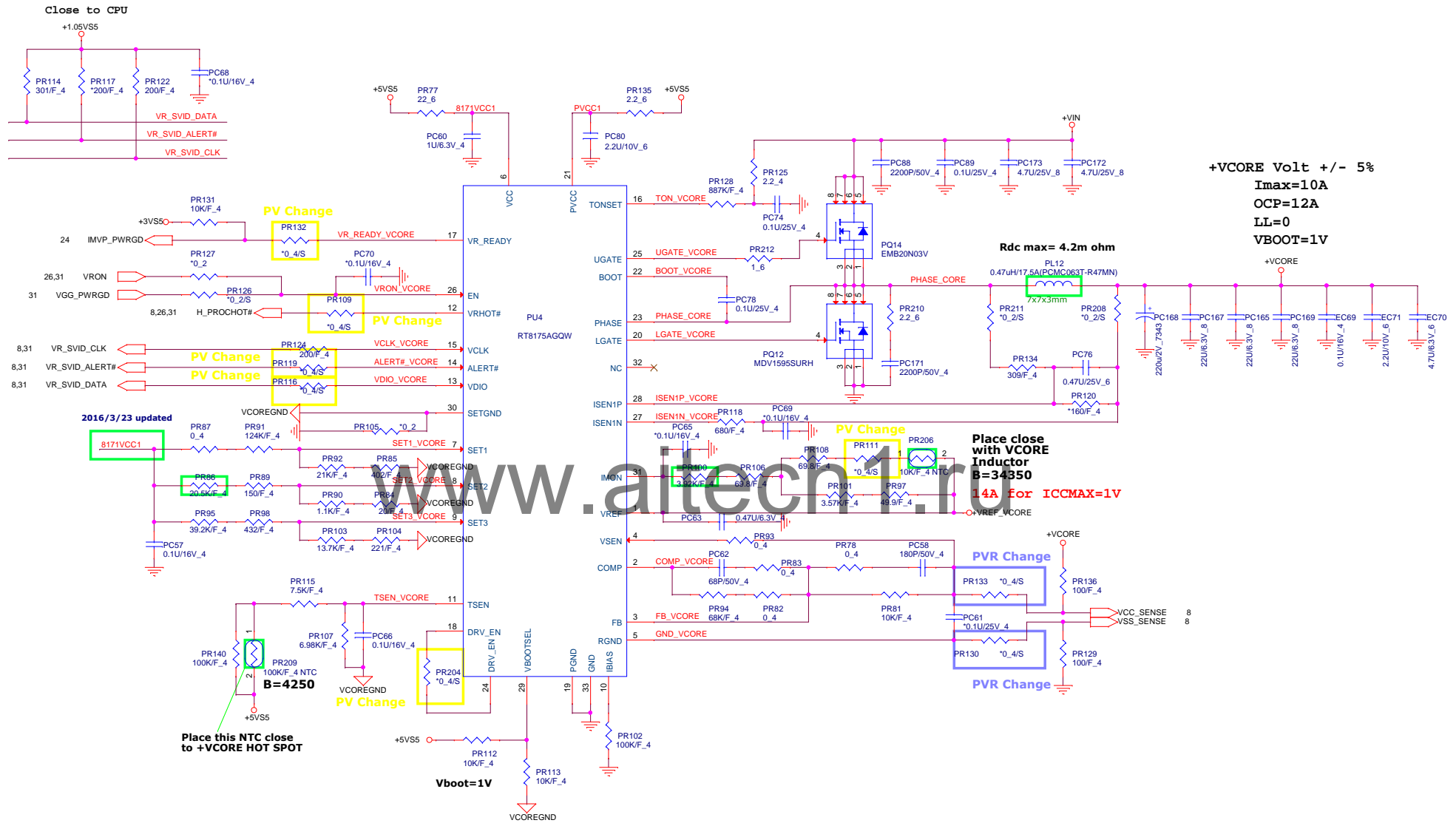




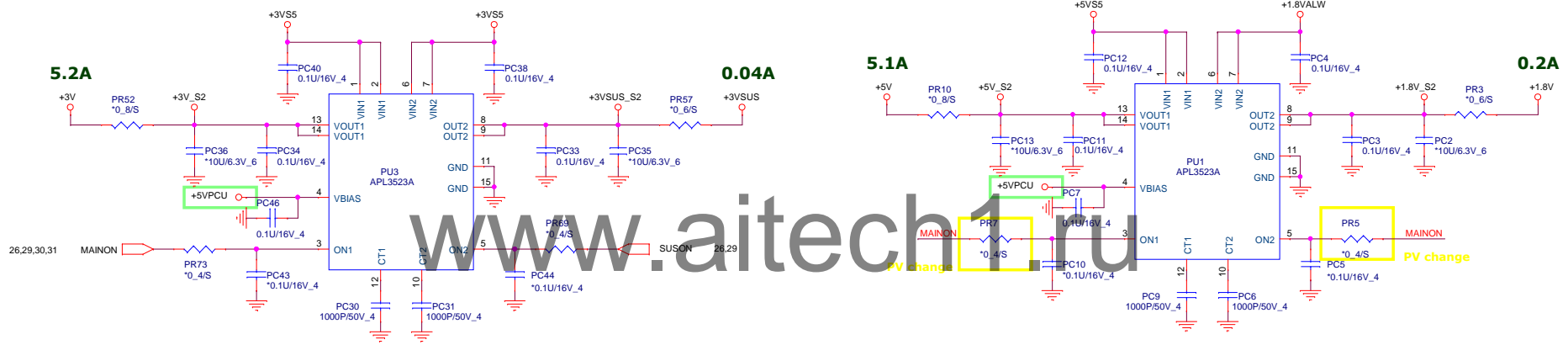


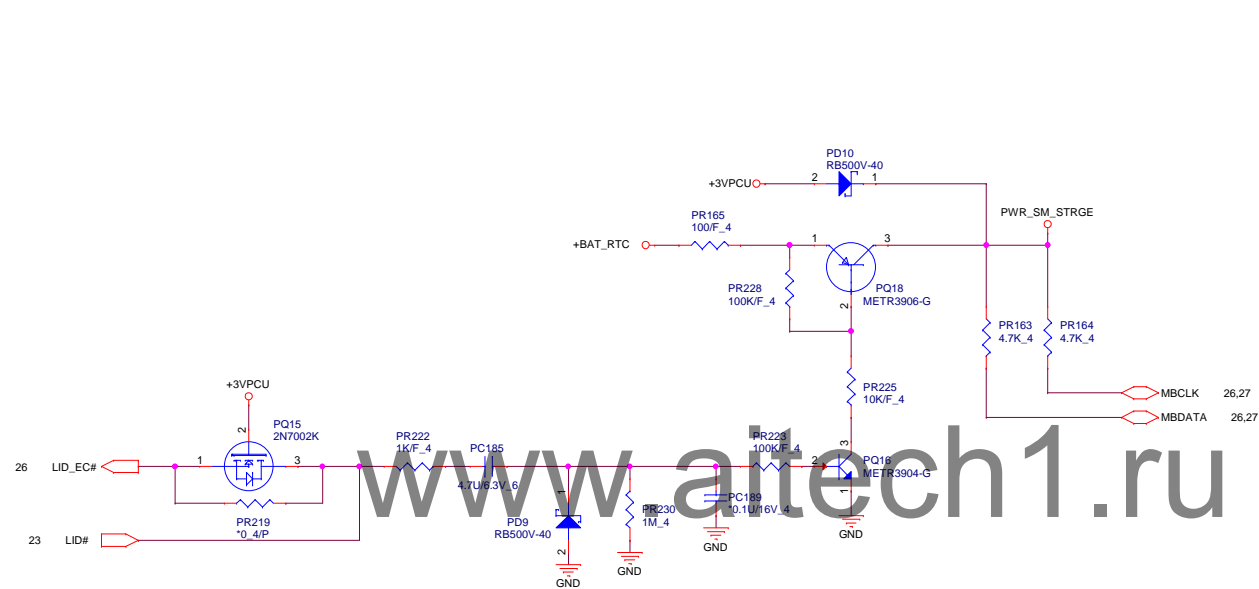
+3VS5	2,35,10,15,20,23,24,25,26,28,31,32,33
+1.6VALW	33
+1.8VS5	4,5,6,7,8,10,12,14,19,22,23,24,26,31
+3VS5_PRIME	10,26
+1.5V	10,17
+1.24VS5	10,25
+1.15VS5	9,30
+5VPCU	27,28,30,33
+1.05VS5	8,9,31,32
+1.15VS5	9,30
+5VPCU	27,28,30,33
+1.8V	4,5,14,19,22,23,24,33





- +3V 4,14,15,16,17,19,21,22,23,24,25,26
- +5V 17,22
- +3VSUS 23
- +5VPCU 27,28,30
- +3VS5 2,3,5,10,15,20,23,24,25,26,28,30,31,32





8,10,20,23,24,26,27,28
23,27

+3VPCU
+BAT_RTC

